



Docket No.: 60437(70820)  
(PATENT)

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Patent Application of:  
Teruyuki Maeda

Application No.: 10/735,399

Confirmation No.: 2109

Filed: December 11, 2003

Art Unit: 2814

For: POWER TRANSISTOR AND  
SEMICONDUCTOR INTEGRATED CIRCUIT  
USING THE SAME

Examiner: L. Pham

**REQUEST FOR EXTENSION OF TIME**

MS Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

Applicant hereby petitions for a one month extension of time to and including November 10, 2006 to respond to the Office Action mailed July 10, 2006.

This petition is being filed in order to ensure copendency with application being filed concurrently herewith.

In the event that a further petition for an extension of time is required to be submitted at this time, applicant hereby petitions under 37 CFR 1.136(a) for an extension of time for as many months as are required to ensure that the above-identified application does not become abandoned.

Please charge our Deposit Account No. 04-1105 in the amount of \$120.00 covering the fee set forth in 37 CFR 1.17(a)(1). The Director is hereby authorized to charge any deficiency in the fees filed, asserted to be filed or which should have been filed herewith (or with any paper hereafter filed in this application by this firm) to our

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Application No.: 10/735,399

2

Docket No.: 60437(70820)

Deposit Account No. 04-1105, under Order No. 60437(70820). A duplicate copy of this paper is enclosed.

Dated: November 10, 2006

Respectfully submitted,

By George Chaclos  
George N. Chaclos

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